

EXPEDITED PROCEDURE – EXAMINING GROUP 2825

S/N 10/606,868

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	William J. Grundmann	Examiner:	Stacy A Whitmore
Serial No.:	10/606,868	Group Art Unit:	2825
Filed:	June 26, 2003	Docket No.:	884.A38US1
Title:	METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed February 28, 2006, please amend the application as follows:

DO NOT
ENTER
J.W.
4/27/06